



"Empowerment through quality technical education"  
Dr D Y Patil Educational Enterprises Charitable Trust's

**Ajeenkya D Y Patil Group of Institution's Technical Campus**  
**Dr D Y PATIL SCHOOL OF ENGINEERING**

(Approved by AICTE, New Delhi Recognized by Govt. of Maharashtra, Affiliated to Savitribai Phule Pune University)  
AISHE Code: C-46648 DTE Code: EN6732 SPPU PUN Code: CEGP015720  
(Accredited by NAAC)

**“Mechanism of Internal Assessment is transparent & robust in terms of frequency of mode”**

Dr. D. Y. Patil School of Engineering has an internal examination and assessment policy. At the beginning of the odd semester the meeting of academic committee is conducted. The Academic Calendar including dates of internal examinations is finalized by that committee.

Academic Calendar is circulated to HoDs, Staff and Students. The Department Academic coordinator along with Internal Exam coordinator detailed time table is prepared one week before the commencement of internal examinations.

The internal examinations planned as Summative Test covering unit-1 and Unit Test-1 on units-2 & 3 and Unit Test-2 on units 4, 5 & 6. Summative Test being conducted on department level. Marks of unit tests are being used in STAL and CO-PO mapping.

To conduct the internal examinations with transparency, the nature of question paper is discussed before the internal examinations with students. The assessment done is explained to the students and areas of improvement are shown. Assessment of doubtful can be reassessed by another teacher / HoD. The question paper is discussed by the subject teacher with the students after internal examinations.

**Prof. P. G. Karajagi**  
**Internal Examination Coordinator**



**Dr. F.B. Sayyad**  
**Principal**

**Principal**  
**Dr. D. Y. Patil School of Engineering**  
**Lohegaon, Pune.**